

<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	<b>Atty Docket No.</b>	ALTRP134/ A01466
	<b>Application No.:</b>	10/815,478
	<b>Applicant</b>	BALL, James L.
	<b>Filing Date</b>	March 31, 2004
	<b>Group</b>	2181
	<b>Conf. No.</b>	6370
		Page 1 of 1
	<b>Submitted on:</b>	November 6, 2008

#### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	1.						
	2.						
	3.						
	4.						

#### Foreign Patent or Published Foreign Patent Application

Examiner Initial		Document No.	Publication Date	Country or Patent Office	Class	Sub-Class	<u>Translation</u>	
	5.						Yes	No
	6.							
	7.							
	8.							

#### Other Documents

Examiner Initial	No.	Author, Title, Place (e.g. Journal) of Publication, Date
/BG/	9.	WOLF, Stanley et al. "Silicon Processing For The VLSI Era, Volume 1: Process Technology", 1986, Lattice Press, 2 pages.
/BG/	10.	Altera Corporation, Nios II Processor Reference Handbook, Chapter 20 Instruction Set Reference, pages 20-1 thru 20-102, December 2004, San Jose, California
/BG/	11.	Final Office Action dated July 30, 2008 in U.S. Application No.: 11/076,307; (ALTRP155)
	12.	
	13.	
	14.	
	15.	

<b>Examiner</b> /Benjamin Geib/	<b>Date Considered</b> 01/15/2009
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Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.